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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/282,144 10/29/2002 PAT 6,732,344 **EO**

** FOREIGN APPLICATIONS *****
 JAPAN 2001-331294 10/29/2001 **EO**

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials EO				

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TITLE
 Semiconductor integrated circuit device and standard cell placement design method

FILING FEE RECEIVED 970	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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